

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	STRUCTURE AND METHOD OF MAKING STRAINED SEMICONDUCTOR CMOS TRANSISTORS HAVING LATTICE-MISMATCHED SOURCE AND DRAIN REGIONS	
Application Number :		
Date :		
First Named Applicant:	Huajie Chen	
Confirmation Number:		
Attorney Docket Number:	FIS920030199US1	
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Documents being submitted:	Files
us-assignment	FIS920030199US1-usassn.xml us-assignment.xsl us-assignment.dtd FIS920030199US1Assignment111.tif FIS920030199US1Assignment212.tif
us-request	FIS920030199US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	FIS920030199US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	FIS920030199US1Declaration410.tif
us-declaration	FIS920030199US1Declaration39.tif
us-declaration	FIS920030199US1Declaration28.tif
us-declaration	FIS920030199US1Declaration17.tif
application-body	FIS920030199US1-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd FIS920030199US1Drawing1.tif FIS920030199US1Drawing2.tif FIS920030199US1Drawing3.tif FIS920030199US1Drawing4.tif FIS920030199US1Drawing5.tif

Comments

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